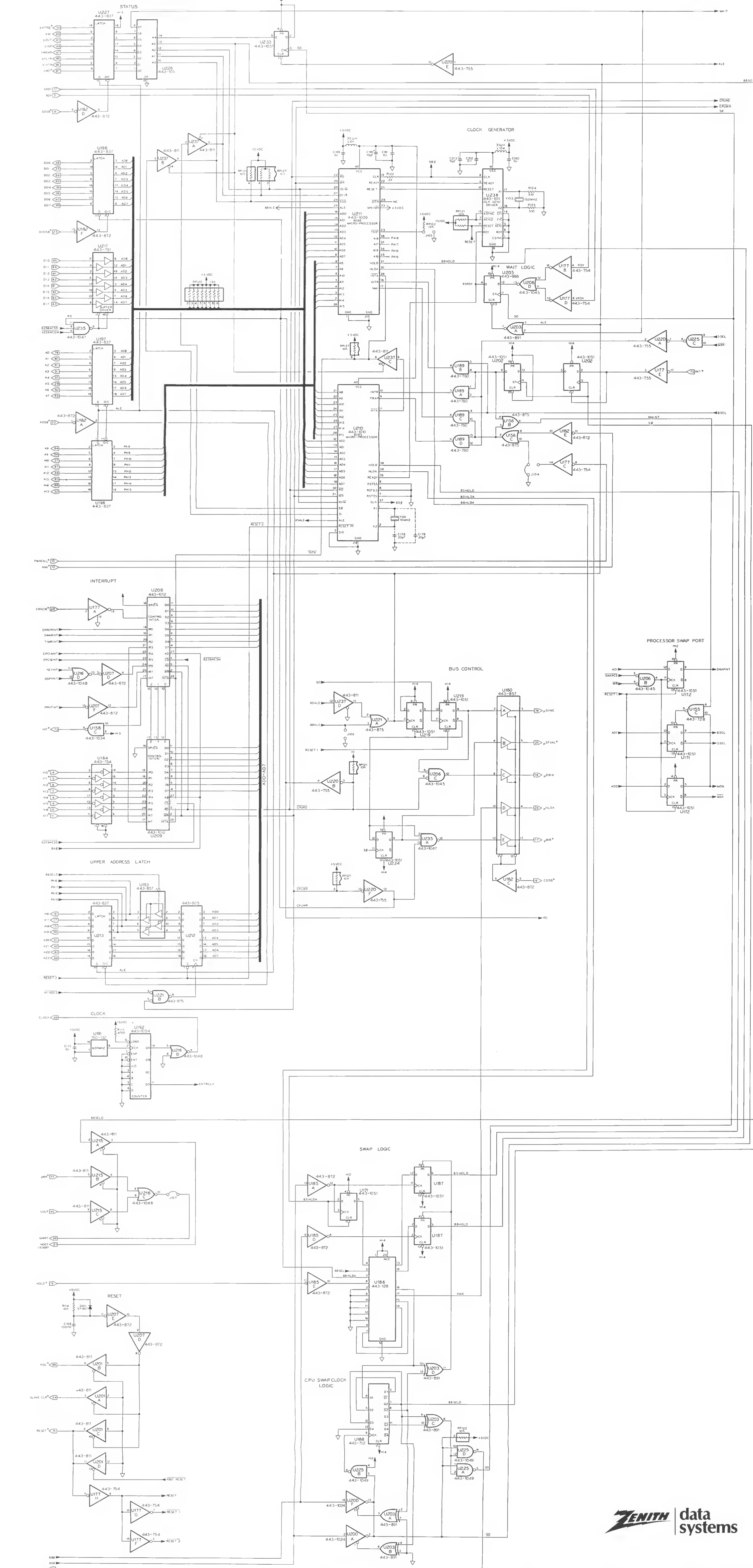
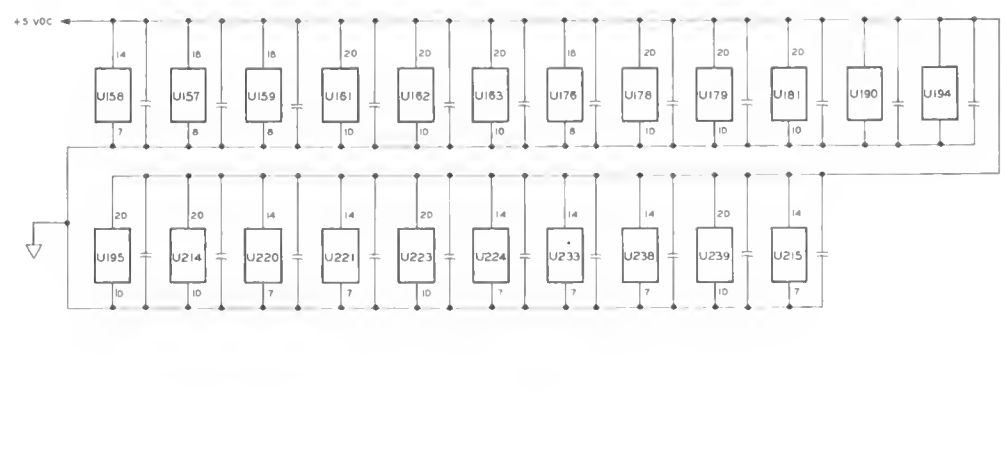
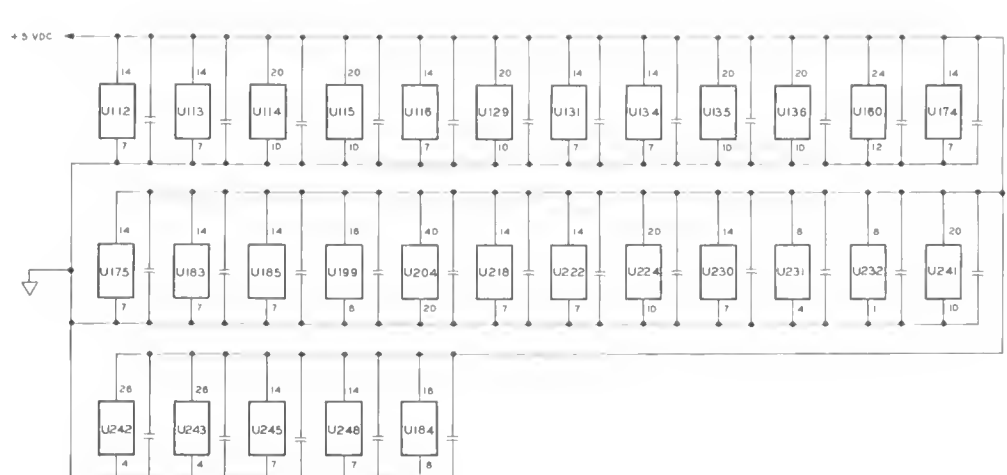
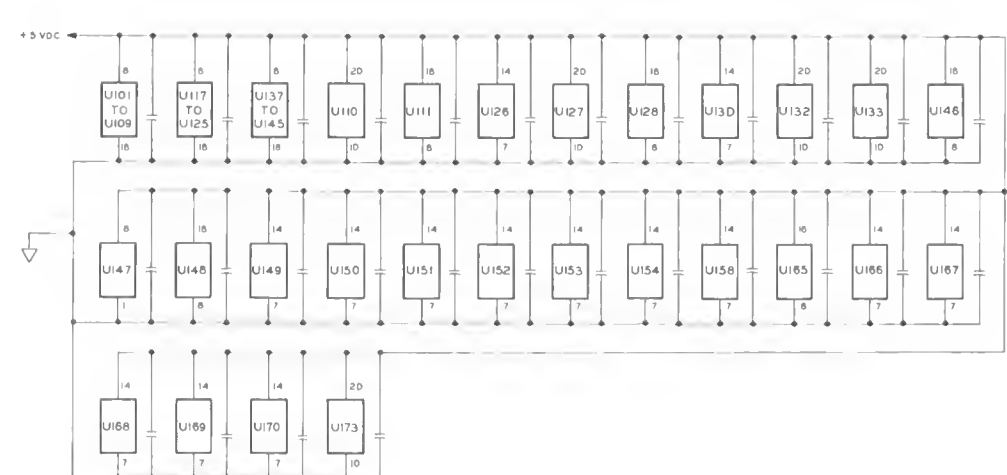
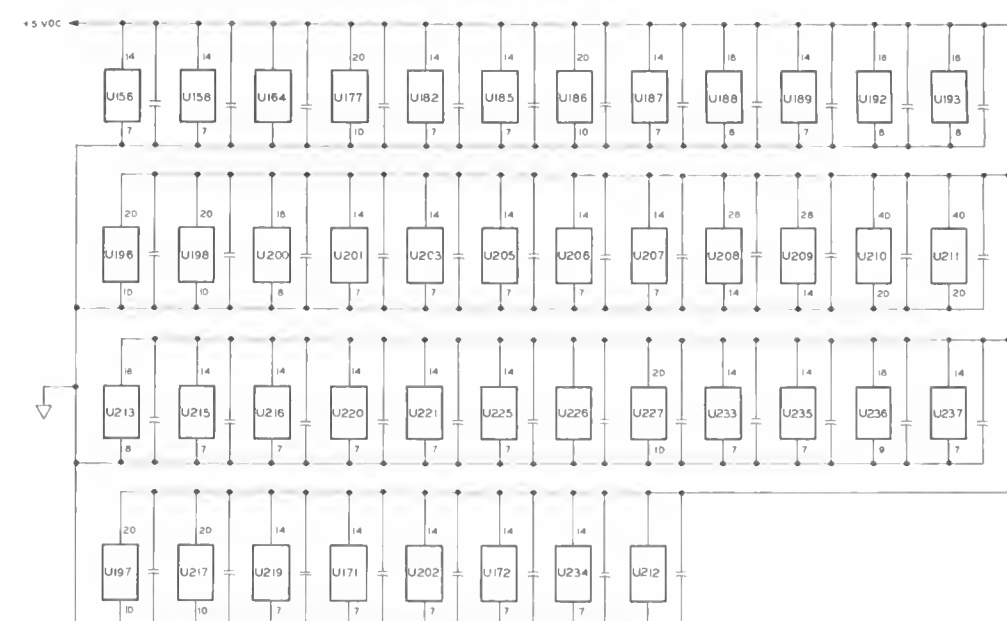
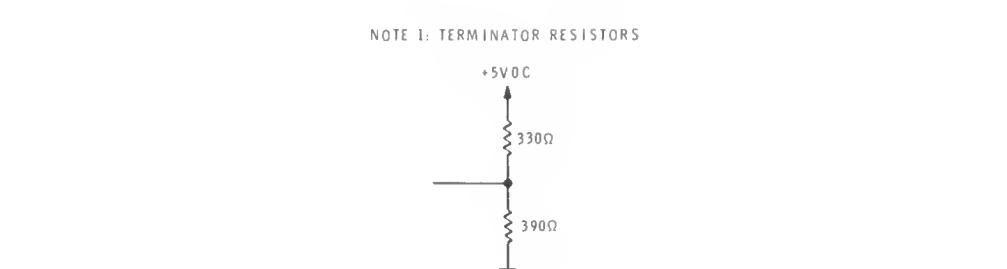


PCB SIGNAL	COMPONENT	VALUE	TERMINATOR
1	10K	10K	
2	10K	10K	
3	10K	10K	
4	10K	10K	
5	10K	10K	
6	10K	10K	
7	10K	10K	
8	10K	10K	
9	10K	10K	
10	10K	10K	
11	10K	10K	
12	10K	10K	
13	10K	10K	
14	10K	10K	
15	10K	10K	
16	10K	10K	
17	10K	10K	
18	10K	10K	
19	10K	10K	
20	10K	10K	
21	10K	10K	
22	10K	10K	
23	10K	10K	
24	10K	10K	
25	10K	10K	
26	10K	10K	
27	10K	10K	
28	10K	10K	
29	10K	10K	
30	10K	10K	
31	10K	10K	
32	10K	10K	
33	10K	10K	
34	10K	10K	
35	10K	10K	
36	10K	10K	
37	10K	10K	
38	10K	10K	
39	10K	10K	
40	10K	10K	
41	10K	10K	
42	10K	10K	
43	10K	10K	
44	10K	10K	
45	10K	10K	
46	10K	10K	
47	10K	10K	
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49	10K	10K	
50	10K	10K	
51	10K	10K	
52	10K	10K	
53	10K	10K	
54	10K	10K	
55	10K	10K	
56	10K	10K	
57	10K	10K	
58	10K	10K	
59	10K	10K	
60	10K	10K	
61	10K	10K	
62	10K	10K	
63	10K	10K	
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77	10K	10K	
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79	10K	10K	
80	10K	10K	
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85	10K	10K	
86	10K	10K	
87	10K	10K	
88	10K	10K	
89	10K	10K	
90	10K	10K	
91	10K	10K	
92	10K	10K	
93	10K	10K	
94	10K	10K	
95	10K	10K	
96	10K	10K	
97	10K	10K	
98	10K	10K	
99	10K	10K	
100	10K	10K	



**NOTES:**

- ALL RESISTOR VALUES ARE IN OHMS (K = 1,000, M = 1,000,000). ALL RESISTORS ARE 1/4 WATT, 5% UNLESS OTHERWISE SPECIFIED.
- ALL CAPACITOR VALUES ARE IN  $\mu$ F (MICROFARADS), UNLESS OTHERWISE SPECIFIED.
- REFER TO THE CIRCUIT BOARD X-RAY VIEWS FOR THE PHYSICAL LOCATION OF PARTS.

**LEGEND:**

- CHASSIS GROUND
- CIRCUIT BOARD GROUND
- DIRECTION
- SIGNAL FROM S-100 BUS
- SIGNAL TO S-100 BUS
- MECHANICAL CONNECTION
- MALE CONNECTION
- FEMALE CONNECTION
- NO CONNECTION
- CONNECTION
- CALIBRATION TEST POINT

**PARTS ORDERING INFORMATION:**

If you order a part from Zenith Data Systems, use the (HE) prefix. Example: HE 443-750

If you order a part from Heath Company, DO NOT use the (HE) prefix. Example: 443-750

For semiconductor type numbers (Example: 74LS153), refer to the "Semiconductor Identification Chart."

**INDEX:**

Sheet 1 of 4  
8085, 8085, clock generator, interrupt, CPU clock swapping, swap logic, reset, clock

Sheet 2 of 4  
system configuration DIP switch, E-clock, I/O decoder

Sheet 3 of 4  
address multiplexer, wait state generator, RAM, parity generator, arbitration and TIC timing

Sheet 4 of 4  
serial ports (modem, printer), parallel ports, keyboard encoder, keyboard matrix, timer

**Z-100 COMPUTER  
MAIN BOARD SCHEMATIC**

**SHEET 1 OF 4**

Part Number 585-0018-02  
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If you order a part from Zenith Data Systems, use the (HE) prefix. Example:  
HE 443-730

If you order a part from Heath Company, DO NOT use the (HE) prefix. Example:  
443-730

---

For semiconductor type numbers (Example: 74LS153), refer to the "Semiconductor Identification Chart."

**Sheet 1 of 4**  
8088, 8085, clock generator, interrupt, CPU clock swapping, swap logic, reset, clock

**Sheet 2 of 4**  
system configuration DIP switch, E-clock, I/O decoder












**Sheet 3 of 4**  
address multiplexer, wait state generator, RAM, parity generator, arbitration and TIC timing

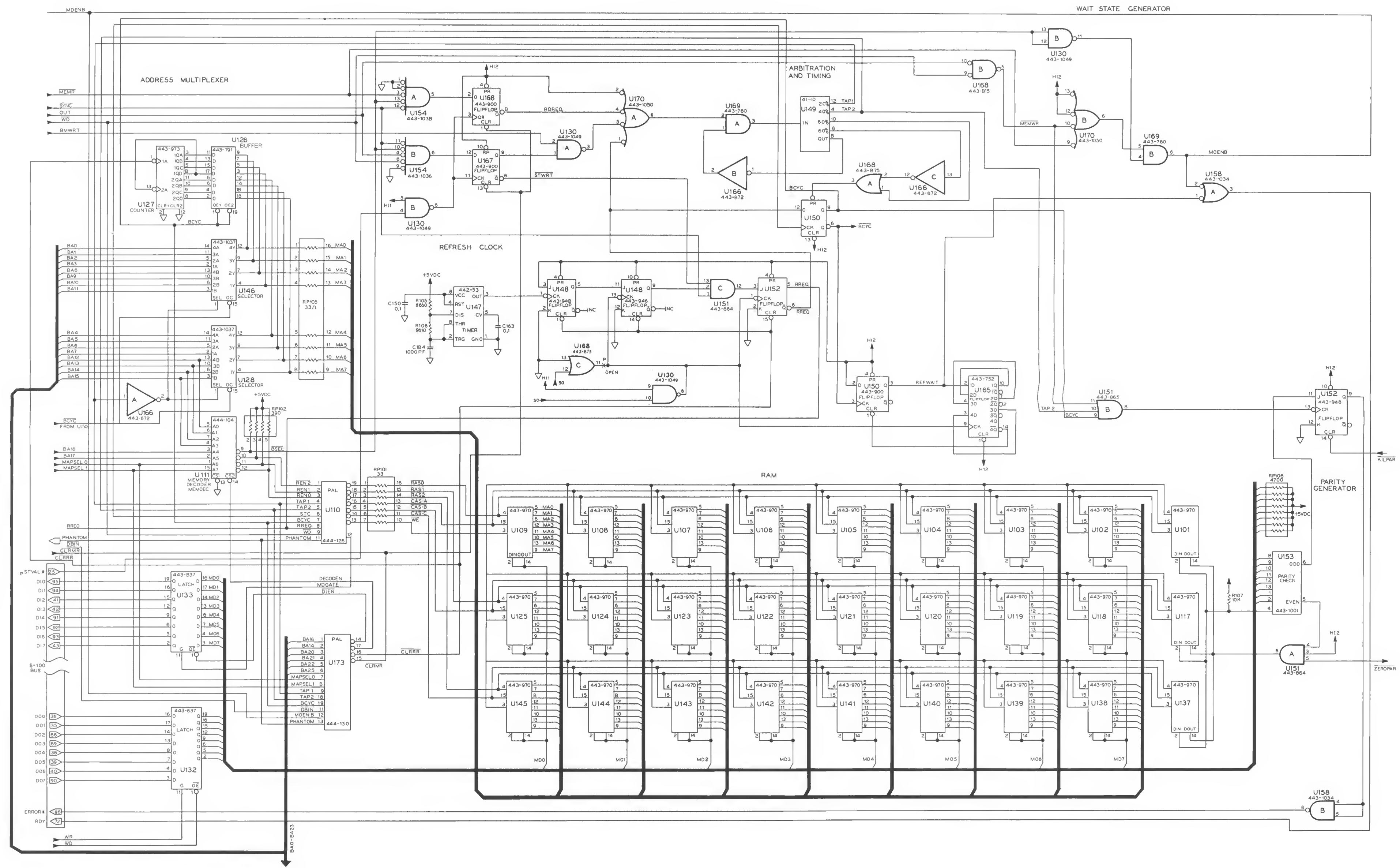
**Sheet 4 of 4**  
serial ports (modem, printer), parallel ports, keyboard encoder, keyboard matrix, timer

**SHEET 2 OF 4**

Part Number 585-0018-02

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- NOTES:**
1. ALL RESISTOR VALUES ARE IN OHMS ( $k = 1,000$ ,  $M = 1,000,000$ )  
ALL RESISTORS ARE 1/4-WATT, 5% UNLESS OTHERWISE SPECIFIED.
  2. ALL CAPACITOR VALUES ARE IN  $\mu F$  (MICROFARADS), UNLESS OTHERWISE SPECIFIED.
  3. REFER TO THE CIRCUIT BOARD X-RAY VIEWS FOR THE PHYSICAL LOCATION OF PARTS.
- LEGEND:**
1.  CHASSIS GROUND
  2.  CIRCUIT BOARD GROUND
  3.  DIRECTION
  4.  SIGNAL FROM S-100 BUS
  5.  SIGNAL TO S-100 BUS
  6.  MECHANICAL CONNECTION
  7.  MALE CONNECTION
  8.  FEMALE CONNECTION
  9.  NO CONNECTION
  10.  CONNECTION
  11.  CALIBRATION OR A TEST POINT



**ZENITH** data systems

**NOTES:**

1. ALL RESISTOR VALUES ARE IN OHMS (K=1,000, M=1,000,000). ALL RESISTORS ARE 1/4-WATT, 5% UNLESS OTHERWISE SPECIFIED.
2. ALL CAPACITOR VALUES ARE IN  $\mu$ F (MICROFARADS), UNLESS OTHERWISE SPECIFIED.
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**LEGEND:**

1. CHASSIS GROUND
2. CIRCUIT BOARD GROUND
3. DIRECTION
4. SIGNAL FROM S-100 BUS
5. SIGNAL TO S-100 BUS
6. MECHANICAL CONNECTION
7. MALE CONNECTION
8. FEMALE CONNECTION
9. NO CONNECTION
10. CONNECTION
11. CALIBRATION OR A TEST POINT

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For semiconductor type numbers (Example: 74LS153), refer to the "Semiconductor Identification Chart."

**INDEX:**

- Sheet 1 of 4  
8088, 8085, clock generator, interrupt, CPU clock swapping, swap logic, reset, clock
- Sheet 2 of 4  
system configuration DIP switch, E-clock, I/O decoder
- Sheet 3 of 4  
address multiplexer, wait state generator, RAM, parity generator, arbitration and TIC timing
- Sheet 4 of 4  
serial ports (modem, printer), parallel ports, keyboard encoder, keyboard matrix, timer

**Z-100 COMPUTER  
MAIN BOARD SCHEMATIC  
SHEET 3 of 4**



1.  CHASSIS GROUND
2.  CIRCUIT BOARD GROUND
3.  DIRECTION
4.  SIGNAL FROM S-100 BUS
5.  SIGNAL TO S-100 BUS
6.  MECHANICAL CONNECTION
7.  MALE CONNECTION
8.  FEMALE CONNECTION
9.  NO CONNECTION
10.  CONNECTION

For semiconductor type numbers (Example: 74LS153), refer to the "Semiconductor Identification Chart."

8088, 8085, clock generator, interrupt, CPU clock swapping, swap logic, reset clock

system configuration DIP switch, E-clock, I/O decoder

Sheet 3 of 4

address multiplexer, wait state generator, RAM, parity generator.

serial ports (modem, printer), parallel ports, keyboard encoder,

## Z-100 COMPUTER MAIN BOARD SCHEMATIC

**SHEET 4 OF 4**